Preferred Device

SWITCHMODE™ Schottky Power Rectifier

The SWITCHMODE Power Rectifier employs the Schottky Barrier principle in a large area metal-to-silicon power diode. State-of-the-art geometry features epitaxial construction with oxide passivation and metal overlay contact. Ideally suited for use as rectifiers in very low-voltage, high-frequency switching power supplies, free wheeling diodes and polarity protection diodes.

- Highly Stable Oxide Passivated Junction
- Very Low Forward Voltage Drop
- Matched Dual Die Construction
- High Junction Temperature Capability
- High dv/dt Capability
- Excellent Ability to Withstand Reverse Avalanche Energy Transients
- Guardring for Stress Protection
- Epoxy Meets UL94, V_O at 1/8"
- Electrically Isolated. No Isolation Hardware Required.
- UL Recognized File #E69369 (Note 1.)

Mechanical Characteristics

- Case: Epoxy, Molded
- Weight: 1.9 grams (approximately)
- Finish: All External Surfaces Corrosion Resistant and Terminal Leads are Readily Solderable
- Lead Temperature for Soldering Purposes: 260°C Max. for 10 Seconds
- Shipped 50 units per plastic tube
- Marking: B20100

MAXIMUM RATINGS

Please See the Table on the Following Page

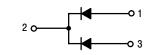
1. UL Recognized mounting method is per Figure 4.



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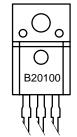






ISOLATED TO-220 CASE 221D STYLE 3

MARKING DIAGRAM



B20100 = Device Code

ORDERING INFORMATION

Device	Package	Shipping
MBRF20100CT	TO-220	50 Units/Rail

Preferred devices are recommended choices for future use and best overall value.

MAXIMUM RATINGS (Per Leg)

Rating		Symbol	Value	Unit
Peak Repetitive Reverse Voltage Working Peak Reverse Voltage DC Blocking Voltage		V _{RRM} V _{RWM} V _R	100	Volts
Average Rectified Forward Current (Rated V_R), T_C = 133°C	Total Device	I _{F(AV)}	10 20	Amps
Peak Repetitive Forward Current (Rated V _R , Square Wave, 20 kHz), T _C = 133°C		I _{FRM}	20	Amps
Non-repetitive Peak Surge Current (Surge applied at rated load conditions halfwave, single phase, 60 Hz)		I _{FSM}	150	Amps
Peak Repetitive Reverse Surge Current (2.0 µs, 1.0 kHz)		I _{RRM}	0.5	Amp
Operating Junction and Storage Temperature Range		T _J , T _{stg}	– 65 to +150	°C
Voltage Rate of Change (Rated V _R)		dv/dt	10000	V/µs
RMS Isolation Voltage (t = 1.0 second, R.H. \leq 30%, T _A = 25°C) (Note 2.) Per F	Per Figure 3. ïgure 4. (Note 1.) Per Figure 5.	V _{iso1} V _{iso2} V _{iso3}	4500 3500 1500	Volts

THERMAL CHARACTERISTICS (Per Leg)

Maximum Thermal Resistance, Junction to Case	$R_{\theta JC}$	3.5	°C/W
Lead Temperature for Soldering Purposes: 1/8" from Case for 5 Seconds	ΤL	260	°C

ELECTRICAL CHARACTERISTICS (Per Leg)

Characteristic	Symbol	Max	Unit
	VF	0.85 0.75 0.95 0.85	Volts
Maximum Instantaneous Reverse Current (Note 3.) (Rated DC Voltage, $T_C = 25^{\circ}C$) (Rated DC Voltage, $T_C = 125^{\circ}C$)	i _R	0.15 150	mA

1. UL Recognized mounting method is per Figure 4.2. Proper strike and creepage distance must be provided.3. Pulse Test: Pulse Width = $300 \ \mu s$, Duty Cycle $\le 2.0\%$

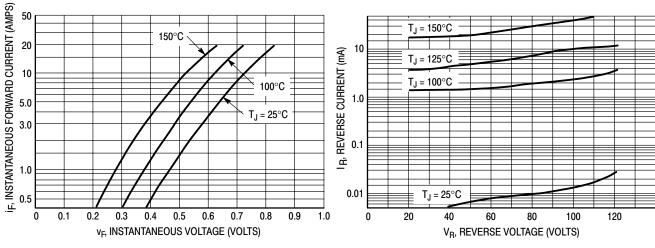


Figure 1. Typical Forward Voltage Per Diode



TEST CONDITIONS FOR ISOLATION TESTS*

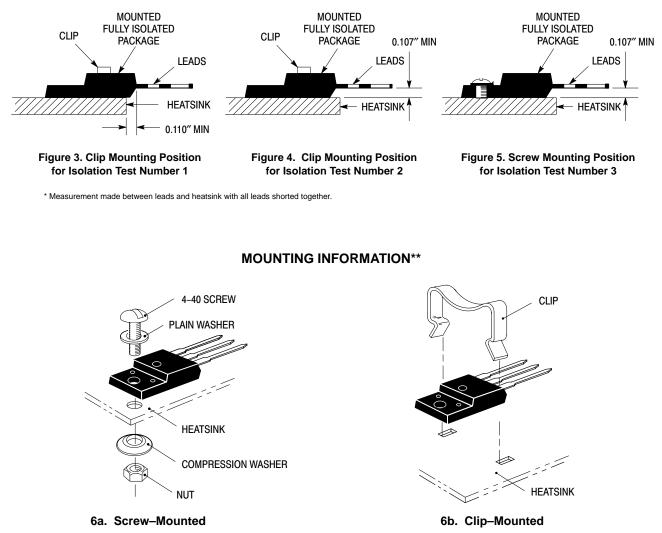


Figure 6. Typical Mounting Techniques

Laboratory tests on a limited number of samples indicate, when using the screw and compression washer mounting technique, a screw torque of 6 to 8 in \cdot lbs is sufficient to provide maximum power dissipation capability. The compression washer helps to maintain a constant pressure on the package over time and during large temperature excursions.

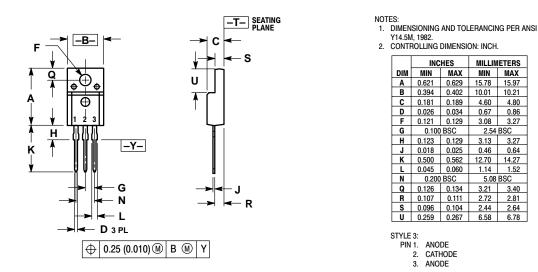
Destructive laboratory tests show that using a hex head 4–40 screw, without washers, and applying a torque in excess of 20 in \cdot lbs will cause the plastic to crack around the mounting hole, resulting in a loss of isolation capability.

Additional tests on slotted 4–40 screws indicate that the screw slot fails between 15 to 20 in \cdot lbs without adversely affecting the package. However, in order to positively ensure the package integrity of the fully isolated device, ON Semiconductor does not recommend exceeding 10 in \cdot lbs of mounting torque under any mounting conditions.

**For more information about mounting power semiconductors see Application Note AN1040.

PACKAGE DIMENSIONS

TO-220 FULLPAK CASE 221D-02 ISSUE D



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